IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): M. KATO et at

Serial No.: Not Yet Assigned

(New Continuation Application under 37 CFR §1.53(b) of

U.S. Application Serial No. 10/351,524)

Filed: On Even Date Herewith

For: NONVOLATILE SEMICONDUCTOR MEMORY

Group: 2824

Examiner: V. LE

<u>UNDER 37 CFR §1.97 & §1.98</u>

MS: Patent Applications Commissioner for Patents POB 1450 Alexandria, VA 22313-1450

March 30, 2004

Sir:

In the matter of the above-identified application, applicants are attaching hereto, in a form equivalent to Form PTO-1449, a listing of all the art documents of record (cited by the Examiner/submitted by applicants) in the chain of continuing applications (as listed on page 1 of the Specification), pursuant to the duty of disclosure/candor requirements.

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified continuation application and, therefore, this IDS meets the requirements pursuant to 37 CFR §1.97(b).

As to the requirement of 37 CFR §1 .98(a)(3) for any concise explanation of relevance to the extent that any of the listed documents is not in the English language, this is met by: the discussion thereof in the prosecution history, the submission of any English language abstracts/translations, and the citation by the Examiner in the prior

application(s).

In keeping with the duty of disclosure/candor requirements, applicants would also like to bring to the Examiner's attention two (2) additional continuation branches having as an original basis, under 35 USC §120, the same basic application as that of the above-identified newly-filed continuation application. The separate branches include (i) U.S. Application Serial No. 09/362,719, filed July 29, 1999, now U.S. Patent 6, 201,735, which was filed as a continuation of U.S. Application Serial No. 09/288,313, filed April 8, 1999, now U.S. Patent 6,101,123; and (ii) U.S. Application Serial No.09/984,918, filed October 31, 2001, now U.S. Patent 6,510,086, which was a continuation of U.S. Application Serial No. 09/881,020, filed June 15, 2001, now U.S. Patent 6,335,880, which was filed as a continuation of U.S. Application Serial No. 09/630,426, filed August 1, 2000, now U.S. Patent 6,272,042. Both prior U.S. Application Serial 09/630,426 (now USP 6,272,042) and Serial No. 09/288,313 (now USP 6,101,123) are included in the series of continuing applications corresponding to the above-identified new continuation application, as listed on page 1 of the present Specification. Copies of the abovementioned related U.S. patents, i.e., USP 6,335,880 and USP 6,510,086, were submitted in the prior, parent application Serial No. 10,351,524. It is noted that the listing of art documents in this IDS submission is inclusive of all art documents of record not only in the corresponding series of continuing applications but, also, with regard to the abovementioned two additional separate continuation branchings.

It is understood that since copies of the listed documents should be available in the file wrapper of the prior application, copies thereof are not included herewith, consistent with 37 CFR §1.98(d) and pursuant to the guidelines set forth in MPEP §609.

It is submitted this IDS is in compliance with the rules of practice as well as with USPTO guidelines. Therefore, applicants respectfully request that it be entered and duly considered by the USPTO. Also, acknowledgment of entry as well as of formal consideration thereof by the Examiner is respectfully requested.

If any costs are due in accordance with the filing of this Information Disclosure Statement, please charge same to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-21 35 (500.32256C11).

Respectfully submitted, ANTONELLI, TERRY, STOUT & KRAUS, LLP

Larry N. Anagnos

Reg. No.-32,392

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	INFORMATION DISCI	_os	URF		Application Number	R53(b) CONT. of 10/351,524	
	STATEMENT BY APPLICANT				Filing Date First Named Inventor	March 30, 2004 Masataka KATO	
					Art Unit	2824	
	(use as many sheets as necessary)				Examiner Name	V. LE	
Sheet	1	of	2		Attorney Docket Number	500.32256C12	

			U.S. PATENT	DOCUMENTS	
Examiner initials'	Cite No. ¹	Document Number Number-Kind Code ² (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 4,924,437	05/08/1990	Paterson et al	
		US- 4,953,129	08/28/1990	Kobayashi et al	
	·	US- 5,053,648	10/01/1991	van der Elshout et al	
		US- 5,060,195	10/22/1991	Gill et al	
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		US- 5,280,454	01/18/1994	Tanaka et al	
		US- 5,297,103	03/22/1994	Higuchi	•
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		US- 5,663,901	09/21/1997	Wallace et al	
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Examiner Initials'	Cite	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T°
	No.'	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)		Applicant of Cited Document		
		JP 62-276878	12/1987			
		JP 04-014871	01/1992			
		JP 03-219496	09/1991			
<u>, ; </u>		JP 01-229497	09/1989			-
		JP 04-007870	01-13-1992	H. Kolzumi et al		AB
		JP 02-260455	10-23-1990	W. Lee et al		AB
		JP 61-127179	06-14-1986	T. Chang et al		AB
		JP 03-250495	11-08-1991	M. Asano		AB
		JP 03-066171	03-20-1991	K. Komori et al		AB
		JP 02-040199	02-08-1990	K. Kobayshi et al		AB
		JP 62-298096	12-25-1987	S. Nabeya et al		AB

Examiner	
	Date
Signature	Considered

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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PTO/SB/08A (10-01)

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	WEODILLE ON DIGG			Application Number	R53(b) CONT.of S.N. 10/351,524		
]	INFORMATION DISCL	_		Filing Date	March 30, 2004		
	STATEMENT BY APP	LIC	ANT	First Named Inventor	Masataka KATO et al		
				Group Art Unit	2824		
	(use as many sheets as nec	essa	ny)	Examiner Name	V. LE		
Sheet	2	of	2	Attorney Docket Number	500.32256C 1 2		

Examiner	Cite	OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the	1
Initials*	No.¹	Item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s),	T
		IEEE Journal of Solid State Circuits, Vol. 26, No. 4, April 1999, pp. 484-491.	Τ
		IEEE Journal of Solid State Circuits, Vol. SC-17, No. 5, Oct. 1982, pp. 821-827.	
·		KUME et al, H., "A 1.28 μm² Contactless Memory Cell Technology for a 3-V Only 64 Mbit EEPROM", International Electron Devices Meeting, 1992, Technical Digest, pp. 24.7.1-24.7.3.	
		Nakayama et al, T "A 60ns 16Mb Flash EEPROM with Program and Erase Sequence Controller", 1991 IEEE ISSCC, pp. 260-261.	
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